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	Filing Date		2003-10-15	
	First Named Inventor	Peter L. Montgomery		
	Art Unit	2183		
	Examiner Name			
	Attorney Docket Number	MS1-1648US		

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SC	1	Groszschadi et al., "Architectural Enhancement for Montgomery Multiplication on Embedded RISC Processors" Lecture Notes in Computer Science-Applied Cryptography and Network Security, vol. 2846, 2003, pp. 418-434, Retrieved from the Internet:www.springerlink.com/media.h4d4wlvwvq7r9ellmv56/contributions/9/0/d/q/90dqk117tj7xy4g.pdf	<input type="checkbox"/>
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SC	3	EPO Communication with Search Report dated March 1, 2006, from counterpart EP patent application, European Patent Application No. 04 018 745.2, copy attached, 4 pages.	<input type="checkbox"/>

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